

Fig.4.

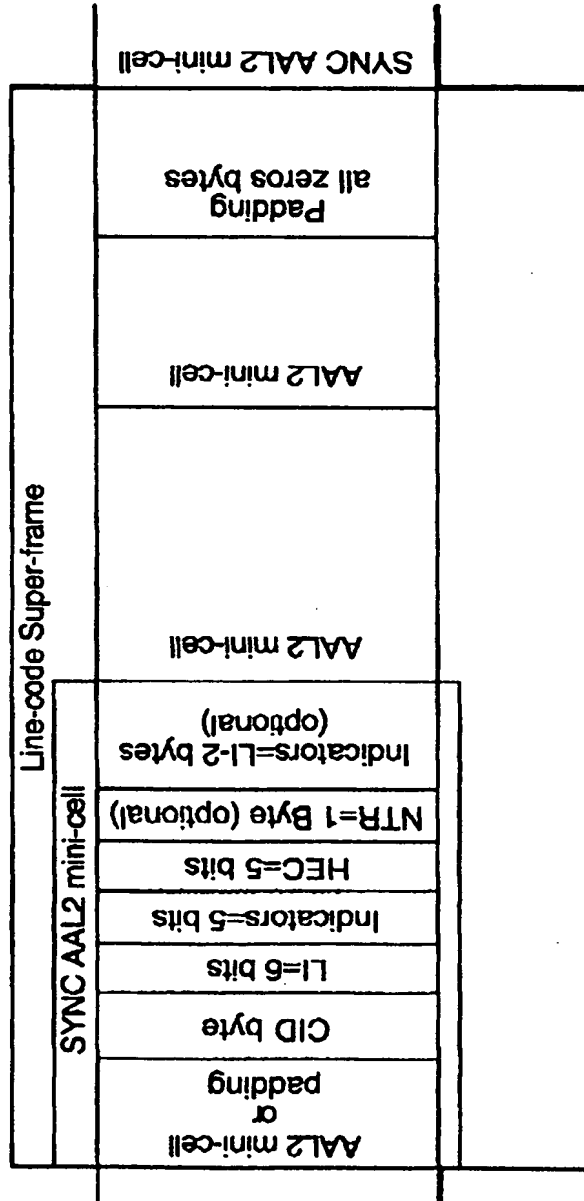
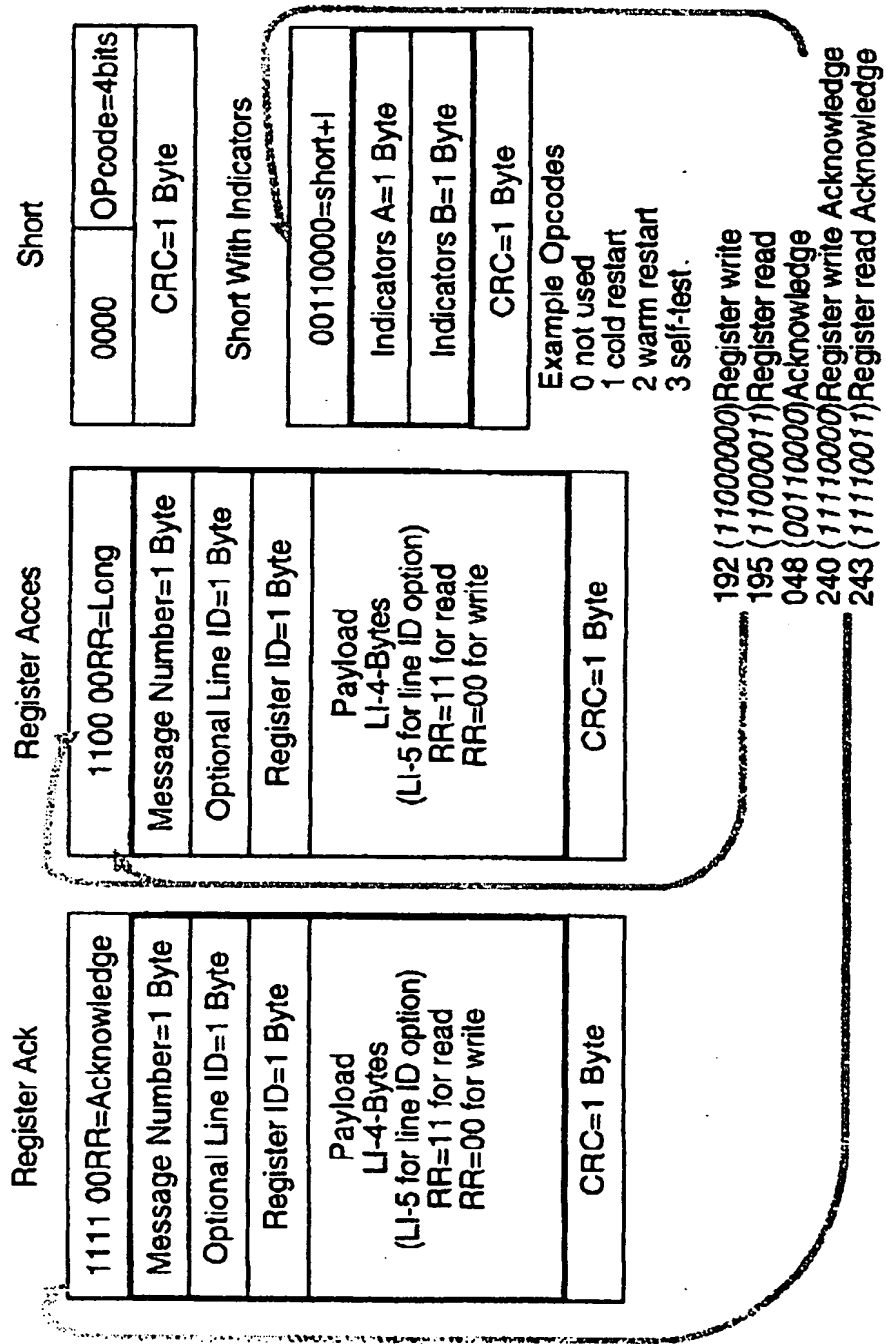
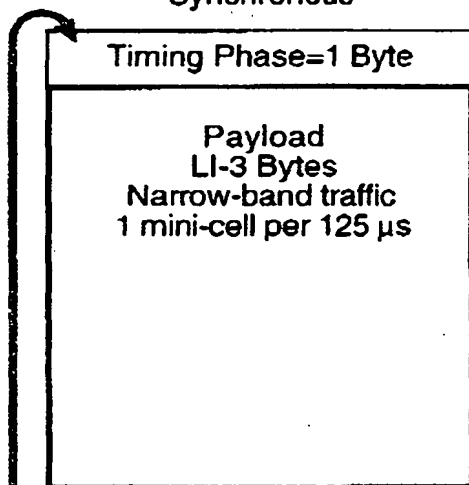


Fig.5.



**Fig.6a.**

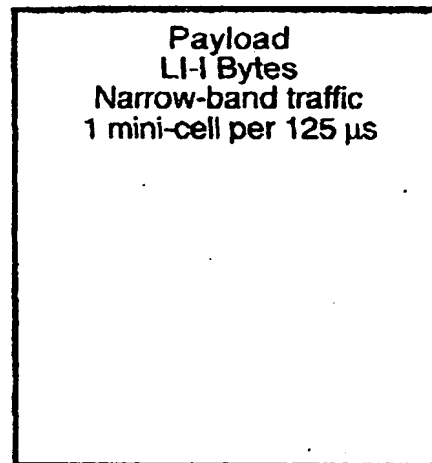
Synchronous



Timing byte is signed integer giving offset from NTR phase

**Fig.6b.**

Pleisochronous



**Fig. 7.**

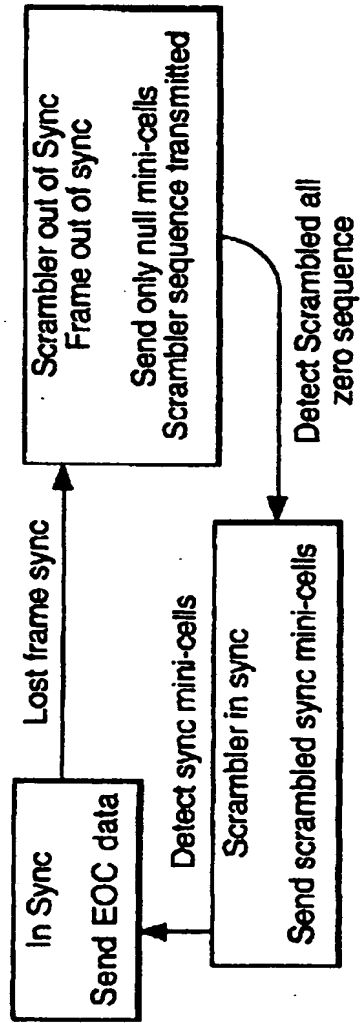


Fig. 8.

